



JSS Mahavidyapeetha

JSS Academy of Technical Education

Dr. Vishnuvardhan Road, Srinivasapura post Bangalore 560060


www. Jssateb.ac.in



FACULTY PROFILE

Department: Electronics & Instrumentation Engineering

1. Personal Details

NAME	Mrs. Shilpa H R	
DEPARTMENT	E&I	
DESIGNATION	Assistant professor	
PHONE	8431843273	
EMAIL ID	shilpahr@jssateb.ac.in	
Date of Joining (JSSATEB)	24-05-2024	

2. Experience

Total Experience in Years	Teaching: 3	Industry: -	Research: -
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3. Qualifications

COURSES	SPECIALIZATION	Year of Award	INSTITUTION	UNIVERSITY
M.Tech	DECS	2018	JNNCE, Shivamogga	Visvesvaraya Technological University (VTU)
B E	Electronics and Communication	2016	PESITM, Shivamogga	Visvesvaraya Technological University (VTU)
2nd P U C	PCMB	2012	Independent PU College, Shivamogga	Department of Pre-University Education
S S L C		2010	Mahaveera Vidyalaya, Shivamogga	Karnataka Secondary Education Examination Board

4. Research & Publications

Papers Published in Web of Science indexed Journals	International:-	National:---
Papers Published in SCOPUS indexed Journals	International:2	National:
Papers Published in other Journals	International:	National: 1
Papers Presented in Conferences / Symposium	International:	National: 3

5. Research Guidance

PhD Guide? Give field & University	NIL	
PhDs / Projects Guided	Ph.D. Awarded: Guiding: NIL	Projects at Master's Level: Projects at Bachelor's Level:

6. Grants

i. Funds Received (Projects)

Project Name	Grant Amount	Date of receiving the Grant	Duration	Grant issuing authority/ Body / Organization
	NIL			

ii. Patents

Sl. No.	Title Details	Details of award of patent / Filed
	NIL	

iii. Consultancy

Title of the work	Amount in Rs.	Date of receiving the Grant	Duration	Grant issuing authority/ Body / Organization
NIL				

7. Awards Received

	NIL
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8. Publications

i. International Journals

Sl. No.	Title of the paper	Name(s) of Author(s)	Name of the Journal	Volume No. Issue No. Year	WOS / Scopus / Both	Impact Factor	Publisher
1	A Novel Approach to Design Braun Array Multiplier Using Parallel Prefix Adders for Parallel Processing Architectures	Shilpa H R, Rashmi D S, Sadiya Rukhsar R, Vidyashree C R, Kunjan D Shinde	ICSCS	2018, CCIS 837, pp. 602–614	-	-	Springer Nature Singapore Pte Ltd
2	<i>Modelling of Adders using CMOS and GDI Logic for Multiplier applications</i>	Shilpa H R, Rashmi D S, Sadiya Rukhsar R, Vidyashree C R, Kunjan D Shinde	IEEE	ICCPCT, 2016	978-1-5090-1277-0		

ii. National Journals

Sl. No.	Title of the paper	Name(s) of Author(s)	Name of the Journal	Volume No. Issue No. Year	WOS / Scopus / Both	Impact Factor	Publisher
1	Performance Analysis Of OFDM System Using Different Types Of Modulation Techniques Under Different Fading Channels And Reducing PAPR Of An OFDM System By Clipping Method	Shilpa H R, & M D Sunil	JNNCE Journal of Engineering and Management	(JJEM), Vol 2, No. 1 January to June 2018.			

iii. Conferences

Sl. No.	Title of the paper	Name(s) of Author(s)	Name of the Journal	Volume No. Issue No. Year	WOS / Scopus	Impact Factor	Publisher
1	<i>Modelling of Adders using CMOS and GDI Logic for Multiplier applications</i>	Shilpa H R, Rashmi D S, Sadiya Rukhsar R, Vidyashree C R, Kunjan D Shinde		NCE TEC-2016,			
2	<i>Modelling of Multipliers using CMOS and GDI Logic for Embedded applications</i>	Shilpa H R, Rashmi D S, Sadiya Rukhsar R, Vidyashree C R, Kunjan D Shinde		NCE TEC-2016,			
3	Performance Analysis Of OFDM System Using Different Types Of Modulation Techniques Under Different Fading Channels And Reducing PAPR Of An OFDM System	Shilpa H R, & M D Sunil	National Conference On Advanced Research In Science, Engineering & Management	ISBN 978-81-926416-6-9,			

	By Clipping Method					
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iv. Workshops /FDP/Conferences Attended

Sl. No.	Name of the workshop / Conference	Organizer	Date
1	Cyber crime & Forensic tools	NITTR Chandigarh	23/5/2022 to 27/5/2022
2	Cryptography for Information Security	NITTR Chandigarh	09/5/2022 to 13/5/2022
3	NEP 2020-Touching the Iceberg	JSSATEB	9th September 2022
4	SECURE SYSTEM ENGINEERING	ISEA, IIT Guwahati	02/06/2022
5	Python programming for Artificial Intelligence(AI) ,High-Performance Computing(HPC), and “Data Analytics	Miety,NIT Warangal	20th june 2022 to 1st July 2022
6	IOT, Deep learning & Cloud computing	TATA Consultancy Services	27 NOV 2019
7	Software defined Networking	BITES	8 TH AUG TO 9 TH AUG 2019
8	"IMPACT OF EV CHARGER ON GRID & RES INTEGRATION"	SJBIT	8/6/2020
9	“AI & Machine Learning”	SIT	12/05/2020
10	Research Opportunities in Modern Antenna Design	PESITM	26/6/2020 TO 28/6/2022
11	New National Education Policy	JSSATEB	
12	VIRTUAL IEEE faculty conclave on non IEEE Faculty MEMBERS	IEEE	25 TH APRIL 2020

v. Workshops / Conference (Organized)

Sl. No.	Name of the workshop / Conference	Organized by	Date	Role
1				

vi. Conference Attended (those sponsored by AICTE / ISTE /IETE /TEQIP or any other sponsoring body)

Sl. No.	Name of the workshop / Conference	Organizer	Date
1			

9. Details of NPTEL / COURSERA courses completed

Sl. No.	Name of the subject	Organized by	Date of completion / Award	Grade / Marks
1				

10. Membership of Professional Bodies:

1.

11. Any other information you will like to share about your professional experience

Mrs. SHILPA H R
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