



JSS MAHAVIDYAPEETHA

# JSS ACADEMY OF TECHNICAL EDUCATION

JSSATE Campus, Uttarahalli – Kengeri Main Road, Bangalore – 560 060

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1. **NAME** : USHA S.M
2. **Date of Birth** : 22<sup>nd</sup> July
3. **Father's Name** : Mallappa
4. **Sex** : Female
5. **Nationality** : INDIAN
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8. **Address**
  - a. **Correspondence** : ASSISTANT PROFESSOR,  
JSS ACADEMY OF TECHNICAL EDUCATION,  
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BENGALURU 560060
  - b. **Permanent Address** : House No. 3560, 20<sup>th</sup> MAIN, 2<sup>nd</sup> STAGE, HOYSALA CIRCLE,  
VIJAYA NAGAR, MYSORE. PIN CODE: 570017
9. **Employment Type** : Permanent

Sl. No.	Degree	Branch	Specialization	Year of passing	University
1	Ph.D.	Electronics & Communication Engineering	VLSI AND COMMUNICATION	REGISTRATION YEAR NOV 2012 (perusing)	VTU Belgaum
2	M.Tech.,	Electronics & Communication Engineering	VLSI Design and Embedded Systems	November 2011	VTU Belgaum
3	B.E.,	Electronics & Communication Engineering	Electronics & Communication Engineering	December 2000	MYSORE University

10. **Educational Qualifications:** (In reverse Chronological Order)

11. **Service Details** (In reverse Chronological Order):

a. **Teaching Experience:**

Sl. No.	From (mm/yy)	To (mm/yy)	No. of years	Degree or Diploma Institution	Designation	Name of the Institute
1	19-01-2004	Till date	12 years +9	Degree	Assistant	JSS Academy of

			months		professor	Technical Education, Bengaluru 560 060
2	30-07-2003	14-01-2004	6 Months	Diploma	Lecturer	JSS Boys. Polytechnique
<b>Total Experience</b>			<b>12 Years + 5 months</b>			

**b. Industrial Experience:**

Sl. NO.	From	To	No. of yrs.	Designation	Name of the Company
1	15-03-2001	27-06-2003	2.3 years	Software Engineer	Vitel Systems Private Ltd

**11. Membership in various University Boards / Committees / Other (Specify):**

Sl. No.	Body	From	To	Name of the University / Institution / Organisation
1	Professional Body ISTE LM 49028	2006	Life member	ISTE Membership
2	Institute of Engineers IEI (M-148200-3)	January 2014	Life member	Institute of Engineers (India)

**12. Field of Expertise / Interest** : Communication  
VLSI Design

13. Details of Publications:

Google scholar link: [https://scholar.google.com/citations?view\\_op=list\\_works&hl=en&user=q8yJPLQAAAAJ](https://scholar.google.com/citations?view_op=list_works&hl=en&user=q8yJPLQAAAAJ)

a. **As on 30-08-2016**

- b. Number of papers published in International Journals : 5
- c. Number of papers published in National Journals : NIL
- d. Number of papers published in International Conferences : 5
- e. Number of papers published in National Conferences : 4

i. **Journals: International**

Sl. No.	Name of Co-Author	Title of Paper	Name of the Journal	Publisher	Publication citation ISSN, ISBN, Vol. No., Issue, pp
1	Dr. K.R Nataraj	Bit Error Rate Analysis Using QAM Modulation for Satellite Communication Link	Science Direct Procedia Technology	Elsevier	Procedia Technology (2016).pp.456-463 DOI: 10.1016/j.protcy.2016.08.132 Published online on -22 <sup>nd</sup> September-2016
2	Dr. K.R Nataraj	Implementation of Digital Modulator Using Digital Multiplier for Wireless Applications in Verilog and Cadence	Science Direct Procedia Technology	Elsevier	Procedia Technology (2016).pp.435-442 DOI: 10.1016/j.protcy.2016.08.129 Published online on -22 <sup>nd</sup> September-2016
3	Dr. K.R. Nataraj	BER Performance of Digital Modulation Schemes With and Without OFDM Model for AWGN, Rayleigh and Rician Channels	International Journal of Science and Research (IJSR)	IJSR	ISSN Online : 2319-7064 Volume No. :4 Issue :11 Month & Year : November 2015 Page Nos. : 330-335

4	Dr. K.R. Nataraj	Theoretical Survey on Low Power and High Throughput QPSK Modulator Designs	International Journal of Electrical, Electronics and Computer Systems (IJEECS)	IRD INDIA	ISSN : 978-2-642-24819-1 Volume No. : 3 Issue : 1 Month & Year : January 2015 Page Nos. :27-30
5	Dr. K.R. Nataraj	High Speed and Low Power QPSK Modulator Design using Verilog and Cadence	International Journal of Advance Engineering and Research Development	IJAERD	ISSN : Online ISSN : Volume No. : 1 Issue : 12 Month & Year : January 2015 Page Nos. : pp. 220-225

**II. Journals: National : Nil**

### **III. Conferences: International**

Sl. No	Name of Co-Author	Title of Paper	Name of the Conference	Publisher	Publication citation ISSN, ISBN, Vol. No., Issue, pp
1	Dr. K.R Nataraj	Bit Error Rate Analysis Using QAM Modulation for Satellite Communication Link	Global Colloquim in Recent Advancement Effectual Researches in Engineering, Science and Technology (RAEREST 2016)	Scence Direct Procedia Technology <b>Elsevier</b>	21 <sup>st</sup> to 22 <sup>nd</sup> April 2016 Palai Kerala
2	Dr. K.R Nataraj	Implementation of Digital Modulator Using Digital Multiplier for Wireless Applications in Verilog and Cadence	Global Colloquim in Recent Advancement Effectual Researches in Engineering, Science and Technology (RAEREST 2016)	Scence Direct Procedia Technology <b>Elsevier</b>	21 <sup>st</sup> to 22 <sup>nd</sup> April 2016 Palai Kerala
3	Dr. K.R Nataraj	Design and Implementation of QAM for Wimax Application using Verilog and Cadence	IEEE sponsored 3 <sup>rd</sup> International Conference on Electronics and Communication Systems	<b>IEEE XPLORE</b>	978-1-4673-7832-1/16/\$31.00 25 <sup>th</sup> & 26 <sup>th</sup> February 2016

			Organized by Dept. of Electronics and Telecommunication Engineering, Karpagam College of Engineering Coimbatore, Tamilnadu India.		
4	Dr. K.R Nataraj  ii.	Analysis and Mitigation of Adjacent and Co-channel Interference on AWGN Channel using 8-PSK Modulation for Data Communication	IEEE International Conference on Wireless Communications, Signal Processing and Networking (WiSPNET). SSN College of Engineering Chennai, India	<b>IEEE XPLORE</b>	978-1-4673-9338-6/16/\$31.00 2016 IEEE 23 <sup>rd</sup> to 25 <sup>th</sup> March 2016.
5	Dr. K.R. Nataraj	Theoretical Survey on Low Power and High Throughput QPSK Modulator Designs	7th International Conference on Electrical, electronics, computing and Communication Systems (EECCS'15)	<b>IRD India</b>	January, 9 <sup>th</sup> 2015, pp.98-101.

#### IV. National Conferences

Sl. No	Name of Co-Author	Title of Paper	Name of the Conference	Publication citation ISSN, ISBN, Vol. No., Issue, pp
1	Dr. K.R. Nataraj	Design and Implementation Of Digital Costas Loop Using FPGA	Proceedings of the Second National conference on Wireless Control and Communication Technologies NCWCT-2014	Organized by the Dept. of Telecommunication Engineering, Dr. AIT Bangalore. ISBN: 978819210471 pp 79-80 on 24 <sup>th</sup> -25 <sup>th</sup> April 2014
2	Prabhudev B.L	Software Defined Radio with an Advanced direct Digital Frequency Synthesizer	Proceedings of the Second National conference on Wireless Control and Communication Technologies NCWCT-2014	Organized by the Dept. of Telecommunication Engineering, Dr. AIT Bangalore. ISBN: 978819210471 pp. 17-19 on 24 <sup>th</sup> -25 <sup>th</sup> April 2014
3	Prabhudev B.L	FPGA Implementation for OFDM SDR using DDFS	National Conference on VLSI, signal and Image Processing	Organized by the Dept. of Electronics and communication Engineering, JSS ATE Bangalore. pp. 142-143 on 22 <sup>nd</sup> -23 <sup>rd</sup> August 2014
4	SM Usha, V Sudheendra J, Venkateawarlu	A Mobile GPRS-Sensor Array for Air Pollution and Weather Monitoring	Wireless Control and Communication Technology (NCWCT-2011)	Proceedings of the Second National conference on NCWCT-2011 at Dr. AIT, Bangalore

## V. Workshops Attended

Sl. No	Title of FDP/Workshop	Organized by	Venue and Date
1	Attended three days' workshop on "SIGNAL, IMAGE PROCESSING & SDR USING LABVIEW" (SIPS-2015)	Department of Electronics and Communication Engineering, JSS Academy of Technical Education, Bangalore	At JSS ATE, Bengaluru, on 22 <sup>nd</sup> , 23 <sup>rd</sup> and 24 <sup>th</sup> June, 2015, Bengaluru
2	Worked as Organizing Committee member for three days' workshop on "SIGNAL, IMAGE PROCESSING & SDR USING LABVIEW" (SIPS-2015)	Department of Electronics and Communication Engineering, JSS Academy of Technical Education, Bangalore	At JSS ATE, Bengaluru, on 22 <sup>nd</sup> , 23 <sup>rd</sup> and 24 <sup>th</sup> June, 2015, Bengaluru
3	Participated one week workshop on "Electronics System Design, Manufacturing & Testing" (ESDMT)	Department of Electronics and Instrumentation Engineering, BMSCE, Bangalore	At BMSCE, Bengaluru, on 27 <sup>th</sup> -31 <sup>th</sup> July 2015, Bengaluru
4	Attended 3 days FDP on "Advanced Design using Cadence Tool Suite"	Department of Electronics and Communication Engineering, JSS Academy of Technical Education, Bangalore	At JSS ATE, Bengaluru, on 16 <sup>th</sup> , 17 <sup>th</sup> and 18 <sup>th</sup> July 2014 Bengaluru
5	Attended 3 days Workshop on "Research Methodologies and Latex"	VTU e-learning Centre, Mysore	At K.S. school of Engineering and Management, 20 <sup>th</sup> to 22 <sup>nd</sup> February, 2014 , Bengaluru
6	Participated and organized two days conference on "VLSI, Signal and Image processing"	Department of Electronics and Communication Engineering, JSS Academy of Technical Education, Bangalore	At JSS ATE, Bengaluru, on 22 <sup>nd</sup> -23 <sup>rd</sup> August 2014, Bengaluru