



JSS MAHAVIDYAPEETHA

JSS ACADEMY OF TECHNICAL EDUCATION

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1. **NAME** : Mrs. Sunita Shirahatti
2. **Date of Birth** : 07-12-1978
3. **Father Name** : Laxman Shirahatti
4. **Sex** : Female
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 - a. **Correspondence** : Assistant Professor,
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 - b. **Permanent Address** : C/o Shrikant M. Navalgi
New no 48, old no 22, Anubhav Nagar,
Nagarbhavi main road,
Bangalore - 560072
9. **Employment Type** : Permanent
10. **Educational Qualifications** : (In reverse Chronological Order)



Sl. No.	Degree	Branch	Specialization	Year of passing	University
1	Ph.D	Electronics & Communication Engineering	Low Power VLSI	persuing	Visveswaraya Technical University, Belagavi
2	M.Tech.	Electronics & Communication Engineering	VTU Belagum (VLSI Design & Embedded Systems)	2009	Visveswaraya Technical University, Belagavi
3	B.E.	Electronics & Communication Engineering	Karnatak University, Dharwad. (Electronics & Communication)	2000	Karnatak University, Dharwad

11. Service Details (In reverse Chronological Order):

a. Teaching Experience:

Sl. No	From (mm/yy)	To (mm/yy)	No. of years	Degree or Diploma Institution	Designation	Name of the Institute
1	27-07-2011	Till date	5.5 years	Degree	Assistant Professor	JSS Academy of Technical Education, Bengaluru 560 060
2	03-10-2001	26-07-2011	9.7 years	Degree	Lecturer	B.V.Bhoomaraddi College of Engineering & Technology, Hubli - 580031
Total Experience			15.2 Years			

Field of Expertise / Interest : Embedded system Design
VLSI for signal processing

12. Details of Publications:

Google scholar link: <https://scholar.google.com/citations?hl=en&user=J6OXwYgAAAAJ>

Citation indices	All	Since 2011
<u>Citations</u>	2	2
h-index	1	1
i10-index	0	0

a. Year: 01-01-2016 to till date

Sl. No.	Name of Co-Author	Title of Paper	Name of the Journal	Publisher	Publication citation ISSN, ISBN, Vol. No., Issue, pp
1.	Kavyashree, saroja Busare	A Qualitative analysis of various adaptive Routing Algorithms	International Research Journal of Engineering & Technology IRJET		ISSN : 2395-0056 Volume No. : 03 Issue : 05 Month & Year : May 2016 Impact Factor: 4.45
2.	Kavyashree, saroja Busare	Architectural Based Congestion Management for Network on Chip implemented on FPGA	IEEE – 2 nd International Conference on Applied and Theoretical Computing and Communication Technology (<i>iCATccT - 2016</i>)		Paper Accepted Registration process completed

b. Year: 01-01-2015 to 31-12-2015

i. Journals: International

Sl. No.	Name of Co-Author	Title of Paper	Name of the Journal	Publisher	Publication citation ISSN, ISBN, Vol. No., Issue, pp
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1	Kavya M	FPGA Implementation of Reversible Multiplier using K-Algorithm for Image Filtering Application	International Journal of VLSI System Design and Communication Systems (IJVDCS)	Blue Eyes Intelligence Engineering & Sciences Publication Pvt. Ltd.	ISSN : 2322-0929 Volume No. : 03 Issue : 03 Month & Year : June 2015 Page Nos. :

c. Year 01-01-2014 to 31-12-2014

iv National conferences / workshops

Sl. No	Name of Co-Author	Title of Paper	Name of the Conference	Publisher	Publication citation ISSN, ISBN, Vol. No., Issue, pp
1	Rakesh M.B	Implementation of 32 bit Reversible Barrel shifter using SCRL gate	National Conference on "VLSI, Signal and Image processing (NCVSIP-14)	JSS Academy of tecgnical Education, Bangalore	22nd and 23rd August 2014.
2	Rakesh M.B	<i>Low Power Reversible Logarithmic Barrel shifter for MAC Applications</i>	National Conference on Recent advances in Computer networks, NCRACN - 2014	RNS Institute of Technology Bangalore , INDIA.	21 st and 22 nd March 2014

d. Year 01-01-2011 to 31-12-2011

ii. Conferences: International

Sl. No	Name of Co-Author	Title of Paper	Name of the Conference	Publisher	Publication citation ISSN, ISBN, Vol. No., Issue, pp
1	Rohini Hongal Halesh M R	1.8V, 5mA Low Drop out Voltage Regulator referenced by a 1.1V	international conference on VLSI,		April 7 th to 9 th ,2011 Pp – 686-691

		curvature corrected Bandgap reference voltage	Communication and Instrumentation [ICVCI-2011]		
2	Rohini Hongal Halesh M R	Understanding linearity range of 1 GHz VCO with and without LDO	international conference on VLSI, Communication and Instrumentation [ICVCI-2011]		April 7 th to 9 th ,2011 Pp – 692-697

e. Year 01-01-2010 to 31-12-2010

iii. Conferences: International

Sl. No	Name of Co-Author	Title of Paper	Name of the Conference	Publisher	Publication citation ISSN, ISBN, Vol. No., Issue, pp
1	Anil Nandi	<i>A Capacitorless low dropout regulator</i>	of International conference on Communication Control and computing Technologies - ICCCCT'10		07-09 October, 2010 pp 256-263

f. Year 01-01-2009 to 31-12-2009

iv. Conferences: International

Sl. No	Name of Co-Author	Title of Paper	Name of the Conference	Publisher	Publication citation ISSN, ISBN, Vol. No., Issue,
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1	Anil Nandi Rohini hongal Rohini korthi	<i>Column Decoder using PTL for Memory</i>	of International conference on advanced Communication and Informatics - ICACI'2009		11-13 January, 2009 pp 9-14.

iv

g. Year 01-01-2008 to 31-12-2008

i. National conferences / workshops

Sl. No	Name of Co-Author	Title of Paper	Name of the Conference	Publisher	Publication citation ISSN, ISBN, Vol. No., Issue, pp
1	Rohini hongal Rohini korthi Anil Nandi	SRAM Memory design with optimized column decoder using PTL	National Level Technical Symposium PRAGATI-08, for PG/Research Students	B. V. Bhomaraddi College of Engg. and Tech., Hubli	18th October 2008