



JSS MAHAVIDYAPEETHA

JSS ACADEMY OF TECHNICAL EDUCATION

JSSATE Campus, Uttarahalli – Kengeri Main Road, Bangalore – 560 060

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1. **NAME** : Prof. Shashidhara H R
2. **Designation** : **Assistant Professor**
3. **Name of the Dept.** : **Electronics and Communication**
4. **Date of joining the Institution:** 11th Aug 2010
5. **Date of Birth** : 26-03-1978
6. **Father's Name** : Ramegowda R
7. **Sex** : Male
8. **Nationality** : INDIAN
9. **Contact No.** : 8095999271
10. **E-mail ID** : **shashidharahr@gmail.com**
 - a. **Correspondance** : D. No. 224/3, 6th Main, 8th Cross,
BEML 5th Stage, 1st Phase,
Rajsrajeshwari Nagar, BENGALURU 560098
 - b. **Permanent Address** : Hirekyathanahally,
Hunsur Tq.
Mysore dist.-571135
11. **Employment Type** : Permanent
12. **Educational Qualifications** : **B.E., M.Tech.**
(In reverse Chronological Order)



Sl. No	Degree	Branch	Specialization	Year of passing	University
2	M.Tech.,	Electronics & Communication Engineering	VLSI Design and Embedded System	August 2008	VTU
3	B.E.,	Electronics & Communication Engineering	Electronics & Communication Engineering	June 2001	Mangalore University

13. **Service / Experience Details** : **13 years**
(In reverse Chronological Order)

a. Teaching Experience:

Sl. No.	From (dd-mm-yy)	To (dd-mm-yy)	No. of years	Degree or Diploma Institution	Designation	Name of the Institute
1	11-08-2010	Till date	6 years	Degree	Assistant Professor	JSS Academy of Technical Education, Bangalore 560 060

2	28-07-2008	31-07-2010	2 Years	Degree	Senior Lecturer	SBMJCE, Bengaluru
3	01/08/2004	31/08/2006	2 Years	Degree	Lecturer	VVCE, Mysore
4	24/07/2001	31/07/2004	3 Years	Degree	Lecturer	CIT, South Coorg
Total Experience			13 Years			

b. Industrial Experience: NIL

Sl. NO	From	To	No. of years	Designation	Name of the Company
1					

14. Membership in various University Boards / Committees / Other (Specify): NIL

Sl. No.	Body	From	To	Name of the University / Institution / Organisation
1	Board of Examiners			
2	Board of Studies			
3	Academic Senate			
4	Executive Council			
5	LIC MPCC			

15. Awards : NIL

16. Details of Books published : "Introduction to SoC", Interline Publisher, 2012

17. Number of Ph.D. Guidance : NIL

18. Field of Expertise / Interest : VLSI Verification, SoC Design and Verification,

TLM Architecture design,

Image Processing,

Computer Networks

Digital Circuits & Logic Design

19. Patents : NIL

20. Projects : NIL

Sl. No.	Name of the project	Principal Investigator / Co-Investigato	Amount sanctioned From	Amount	Date of start of the project	Date of completion of the project	Outcome of the project
1							
2							

3							
4							

21. Details of Publications:

Google scholar link: <https://scholar.google.co.in/citations?user=37WXf6IAAAAJ&hl=en>

As on 30-09-2016

Citation Indices	All	Since 2010
Citations	2	2
h-Index	1	1
i10-Index	0	0

Number of papers published in International Journals : 08

Number of papers published in National Journals : NIL

Number of papers published in International Conferences : 12

Number of papers published in National Conferences : 04

i. Journals: International

Sl. No.	Name of Co-Author	Title of Paper	Name of the Journal	Publisher	Publication citation ISSN, ISBN, Vol. No., Issue, pp
1	Pratheek Kumar T R, Gangadhariah S L	Verification of Deadlock Recovery in NOC using encoding scheme	International Journal of Computer Science and Information Technologies	Computer Science Directory, USA	ISSN: 0975-9646 Volume No. :7 Issue :3 Month & Year : June 2016 Page No.:1586-1588
2	Uma B S and P M Shivakumara Swamy	Implementation of Image Forgery Detection using Lifting Scheme based Dyadic	International Journal of Innovative Research in Science and Engineering	International Journal of Innovative Research in Science and Engineering	ISSN : 2454-9665 Volume No. :2 Issue :6 Month & Year : June 2016 Page Nos. : 78-87

		Framelets			
3	Pratibha and Ravikumar K P	Implementation of Stegangraphic Image Using Lifting Scheme based Symmetric Framelets	International Journal of Information Technology and Computer Engineering (IJITCE)	MEGABIT ACADEMY, Maharashtra ,India	ISSN : 2455-529 Volume No. :8 Issue : NCRASET-16 Month & Year : June 2016 Page Nos. : 41-46
4	P M Shivakumaraswamy, Uma B S, Sowmya R Bangari	Image Forgery Detection using Dyadic Framelets	International Journal of Information Technology and Computer Engineering (IJITCE)	MEGABIT ACADEMY, Maharashtra ,India	ISSN : 2455-529 Volume No. :8 Issue : NCRASET-16 Month & Year : June 2016 Page Nos. : 20-24
5	Dr. A R Aswatha	Comparative Study of Iris Recognition using Neville's Algorithm and Symmetric Framelet	International Journal of Applied Information Systems,	Foudation of Computer Science and Harward University Press, Newyork.	ISSN : 2231-2307 Volume No. :9 Issue :7 Month & Year : Sep 2015 Page Nos. : 11-16
6	Dr. A R Aswatha	Palm Recognition using Kernel Based Approach and Symmetric Mask Discrete Wavelet Transform	International Journal of Informative and Futuristic Research	American Scientific Publishers	ISSN : 2156-7018 Online ISSN : 2156-7026 Volume No. : 2 Issue : 10 Month & Year : June 2015 Page Nos. : 3968-3984
7	Dr. M V Latte	A Multilevel Compression For Test Data Compression	International Journal of Industrial Electronics and Electrical Engineering (IJIEEE)	In association with IRAJ	ISSN : 2349-2402 Volume No. : 3 Issue : 7 Month & Year : June, 2015, Page Nos. : pp. 90-94
8	Dr. M V Latte	A Hardware Implementation of Palm Recognition using 1-D DWT",	International Journal of Systems, Algorithms & Applications	Association of Scholars and Professionals (ASP)	ISSN : 2277-2677 Volume No. : 3 Issue : ICEEC13, Month & Year : June 2013 Page Nos. : pp. 43-45

ii. Interational Conference

Sl. No	Name of Co-Author	Title of Paper	Name of the Conference	Publisher	Publication citation ISSN, ISBN, Vol. No., Issue, pp
1	---	Test Vector Generation Using Different Logic Regression Method	2 nd IEEE International Conference on Applied and theoretical Computing and Communication Technology	IEEE Advanced Technology	21 st to 23 rd July, 2016, SJBIT, Bengaluru, ISBN: 978-1-5090-2399-8/16, PP 756-764
2	Adithya Rangan C K and Vikas Kulkarni	A Theoretical Proposition to Characterize Speed based Performance of Bulk Transaction in USB 3.0 Super Speed Protocol.	2 nd IEEE International Conference on Applied and theoretical Computing and Communication Technology	IEEE Advanced Technology	21 st to 23 rd July, 2016, SJBIT, Bengaluru, ISBN: 978-1-5090-2399-8/16, PP 49-54
3	Dr. A R Aswatha	A Functionally Verified IP For Fingerprint Recognition Using Fast Convolution Method	International Conference on Emerging Trends in Engineering and Technology	IFERP	7 th June 2015, pp 30-34
4	Dr. A R Aswatha	A Novel Approach To Circular Edge Detection For Iris Image Segmentation	IEEE International Conference on Signal and Image Processing-2014 (ICSIP-2014)	IEEE Computer Society	8 th to 10 th Jan 2014, PP 316-320
5	Ashish K Babu, V Nattarasu	Design and Implementation of an arbiter for efficient multisystem interaction with shared resources	International joint conference-2013, held at Goa, India,		on 7 th July 2013, ISBN:978-81-927147-7-6, PP 31-37
6	Dr. A R Aswatha	Digital Design of Face Identification based on Markov Chain Monte Carlo Method	International Conference on Electrical Engineering & Computer Science		2 nd June-2013, Bengaluru, ISBN: 978-93-83060-04-7, PP 9-11
7	Dr. A R Aswatha	A Theoretical Survey on Face Recognition and Markov chains	2013 International Conference on Smart Structures & Systems (ISSS-2013)		March 28 – 29, 2013, Chennai, INDIA, PP 41-45

8	Dr. A R Aswatha	A Theoretical Study on Palm Print Identification	2013 International Conference on Smart Structures & Systems (ISSS-2013)		March 28 – 29, 2013, Chennai, INDIA, PP 46-50
9	Dr. A R Aswatha	Digital Design of finger print recognition based on Eigen Vector transform	Proceedings of International Conference on Electrical and Electronics Engineering (ICEEE-2012)		30 th June 2012, Mysore-India, PP 114-118
10	Dr. A R Aswatha	Hardware Accelerator of finger print recognition based on Eigen Iterative Method	Proceedings of International Conference on Computational Intelligence and Communication (ICCIC-2012)		4 th to 6 th July 2012, SKR College of Engineering, Chennai, PP 26-30
11	Srikanth K M	Hardware Accelerator of finger print recognition based on Eigen Iterative Method	Proceedings of International Conference in Magna on Emerging Engineering Trends (ICMEET2K12)		12 th and 13 th April 2012, Magna College of Engineering, Chennai, PP 353-358
12	H S Kala, B G Shivallelavathi	Hardware Accelerator for Image Watermarking Technique	Proceedings of International Conference on Advances in Electrical, Electronics, Information, Communication and Bio-Informatics (AEEICB'12)		January 2012, Coimbatore Institute of Information Technology, Coimbatore, PP 127-131

iii. National conferences / Symposium / workshops

Sl. No	Name of Co-Author	Title of Paper	Name of the Conference	Publisher	Publication citation ISSN, ISBN, Vol. No., Issue, pp
1	Mohana Sundaram and J.Sudheendra	Vision Based Automatic Parking System	Proceedings of National Conference on on Electronics and Communication	Oxford College of Engineering, Bengaluru	25 th Sep 2016, PP 75-80

2	Sachin J Katharki, Dr. A R Aswath	Intellectual property of fingerprint recognition based on Karhunan- Loeve Trasformation	Proceedings of National Conference on Advanced communication Trends (ACT'12)	R R College of Engineering, Bengaluru	23 rd and 24 th Aug 2012, PP 150-154.
3	Mohan G, Dr. A R Aswath	CMOS based Processing Unit of Bio-Potential Acquisition Device	Proceedings of National Conference on Advanced communication Trends (ACT'12)	R R College of Engineering, Bengaluru	23 rd and 24 th Aug 2012, PP 150-154.
4	DileepKumar K S	A comparative study of Edge Detection using different Discrete Wavelet Transform families	National Conference emerging technology engineering and technology 2012, Bangalore	Global Academy of Technology, Bengaluru	January 20 th -21 st 2012.