



JSS MAHAVIDYAPEETHA

# JSS ACADEMY OF TECHNICAL EDUCATION

JSSATE Campus, Uttarahalli – Kengeri Main Road, Bangalore – 560 060

Phone: 080-28611902, 28612797 Fax: 080-28612706 [www.jssateb.ac.in](http://www.jssateb.ac.in)

1. **NAME** : SAVITHA A. C
2. **Date of Birth** : 1-06-1979
3. **Father's Name** : A.R CHANDRAPPA
4. **Sex** : Female
5. **Nationality** : INDIAN
6. **Contact No.** : ( (M) +91 9739805777
7. **E-mail ID** : savithalm@gmail.com



8. **Address**

- a. **Correspondance** : AP, ECE DEPARTMENT  
JSS ACADEMY OF TECHNICAL EDUCATION,  
UTTARAHALLI – KENGERI MAIN ROAD  
BENGALURU 560060

- b. **Permanent Address** : #12/A, kallleshwara Nilaya, Pavamanapura Layout, Chikkegoudana Palya, R.R.  
Nagar, Bangalura-98

- c. **Employment Type** : Permanent

9. **Educational Qualifications:** (In reverse Chronological Order)

Sl. No.	Degree	Branch	Specialization	Year of passing	University
1	M.Tech.,	Electronics & Communication Engineering	VLSI & EMBEDED DESIGN	2013	V T U
2	B.E.,	Electronics & Communication Engineering	Electronics & Communication Engineering	2001	Kuvempu University

10. **Service Details** (In reverse Chronological Order):

a. **Teaching Experience:**

Sl. No.	From (mm/yy)	To (mm/yy)	No. of years	Degree or Diploma Institution	Designation	Name of the Institute
1	16-08-2007	Till date	7.5 years	Degree	AP	JSS Academy of Technical Education, Bengaluru 560 060
2	01-01-2003	01-09-2003	9 Months	Diploma Institution	Lecturer	Sahydri Polytechnic, Thirthahalli
<b>Total Experience</b>			<b>8.4 Years</b>			

**b. Industrial Experience:**

<b>Sl. NO.</b>	<b>From</b>	<b>To</b>	<b>No. of yrs.</b>	<b>Designation</b>	<b>Name of the Company</b>
1	16-3-2006	16-7-2007	1.4 Months	Software Enginner	SGC,Bangaluru
2	17-9-2003	16--2003	1 year	Graduate Trainee	K.P.C. Linganamakki Power House
Total Experience			2.4 years		

11. Field of Expertise / Interest : Embedded Systems

Micro controller, Micro- processors

Digital Circuits & Logic Design

12. Details of Publications:

Google scholar link: [http://scholar.google.co.in/citations?view\\_op=list\\_works&hl=en&user=0Gh6DnoAAAAJ](http://scholar.google.co.in/citations?view_op=list_works&hl=en&user=0Gh6DnoAAAAJ)

As on 30-11-2015

a. Year: 01-01-2015 to 31-12-2015

i. Journals: International

Sl. No.	Name of Co-Author	Title of Paper	Name of the Journal	Publisher	Publication citation ISSN, ISBN, Vol. No., Issue, pp
1	Siddesh gundagatti	Crosstalk Delay Avoidance in Long on Chip Buses by using different Fibonacci CODEC Techniques	International Journal of Computer Of Application ( IJCA)	IJCA Proceedings NCPSIA 2015: Publication Pvt. Ltd.	ISBN : 973-93-80892-04-4 Volume No. :4 Month & Year : December- 2-2015
2	Ajay Kumar D	Implementation of sub carrier Modulation Technique using Matlab/Simulink	IRJET		ISSN : 2395-0056 PISSN : 2395-0072 Volume No. :3 Month & Year :May 5-2016
3	Apoorva Desai	Domotics using IOT	IJCSE	Accepted and will be published shortly	

ii. Conferences: International

Sl. No	Name of Co-Author	Title of Paper	Name of the Conference	Publisher	Publication citation ISSN, ISBN, Vol. No., Issue, pp
1	Binu B Singh	Preventing Crosstalk Delay in Long on chip buses by using Fibonacci codec mechanism	National Conference on Communication and Image Processing		

### WORKSHOPS ATTENDED

<b>SL. NO</b>	<b>NAME OF THE TEACHER</b>	<b>NAME OF THE WORKSHOP</b>	<b>PLACE</b>	<b>DATE</b>
01	Savitha A C Asst. Prof	“Hands on Training on Analog, Power, Embedded systems and Wireless IOT”	Reva School of Engg, Bangalore	11 <sup>th</sup> to 14 <sup>th</sup> July 2016
02	Savitha A C Asst. Prof	Signal,Image Processing & SDR Using Labview	JSS Academy of Technical Education	22 <sup>nd</sup> to 24 <sup>th</sup> June 2015
03	Savitha A C Asst. Prof.	4 days workshop on PCB Process Automation	PESITM, Shimoga	24 <sup>th</sup> to 27 <sup>th</sup> June 2015
04	Savitha A C Asst. Prof.	Antenna Design and Simulation Techniques	RNS Institute of Technology	16 <sup>th</sup> & 17 <sup>th</sup> June 2015
05	Savitha A C Asst. Prof	Cadence Design Systems India Pvt.Ltd	JSS Academy of Technical Education	16 <sup>th</sup> to 18 <sup>th</sup> July 2014
06	Savitha A C Asst. Prof	Matlab & its Applications to DSP,DIP and Power Electronics	RNS Institute of Technology	25 <sup>th</sup> to 29 <sup>th</sup> July 2011
07	Savitha A C Asst. Prof	Applied Embedded System Design	JSS Academy of Technical Education	3 <sup>rd</sup> to 7 <sup>th</sup> August 2009

