



JSS MAHAVIDYAPEETHA

# JSS ACADEMY OF TECHNICAL EDUCATION

JSSATE Campus, Uttarahalli – Kengeri Main Road, Bangalore – 560 060

Phone: 080-28611902, 28612797 Fax: 080-28612706 [www.jssateb.ac.in](http://www.jssateb.ac.in)

1. **NAME** : Sathish Shet K
2. **Date of Birth** : 15-04-1979
3. **Father's Name** : Upendra Shet K
4. **Sex** : Male
5. **Nationality** : INDIAN
6. **Contact No.** : (P) +91 9448109798
7. **E-mail ID** : satish.personal@gmail.com
8. **Address**
  - a. **Correspondance** : #484,Srinidhi,18<sup>th</sup> cross  
**Poornapragna Layout, Bengaluru**  
**560061**
  - Permanent Address** : Nellikatte Post, Kundapura Taluk  
Udupi District pincode: 576211
9. **Employment Type** : Permanent
10. **Educational Qualifications** : (In reverse Chronological Order)



Sl. No.	Degree	Branch	Specialization	Year of passing	University
1	Ph.D.	Electronics & Communication Engineering	VLSI	Pursuing	VTU
2	M.Tech.,	Electronics & Communication Engineering	VLSI Design & Embedded Systems	May 2006	VTU
3	B.E.,	Electronics & Communication Engineering	Electronics & Communication Engineering	July 2000	Mangalore University

11. **Service Details** (In reverse Chronological Order):

**a. Teaching Experience:**

Sl. No.	From (mm/yy)	To (mm/yy)	No. of years	Degree or Diploma Institution	Designation	Name of the Institute
1	2-02-2012	Till date	4 years	Degree	Assistant Professor	JSS Academy of Technical Education, Bengaluru 560 060
2	22-02-2001	31-07-2006	5.6 Years	Degree	Lecturer	DSCE, Bengaluru
3	08-08-2006	31-01-2012	5.6 years	Degree	Assistant Professoor	SBM Jain college of Enginnering(Now Jain University)
4						
<b>Total Experience</b>			<b>15 Years</b>			

12. **Membership in various University Boards / Committees / Other (Specify):**

Sl. No.	Body	From	To	Name of the University / Institution / Organisation
1	VTU Valuation Unit coordinator at SBMJCE	June 2008	Dec 2009	VTU
2	Deputy Chief Coordinator for VTU exams at JSSATE	Dec 2014 & June 2015		VTU

Field of Expertise / Interest : Digital Signal processing , Image Processing,

VLSI, Embedded Systems

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### 13. Details of Publications:

1. Google scholar link: <http://scholar.google.co.in/citations?hl=en&user=vIW8d3wAAAAJ> ( Internationa Conference)
2. Google Link: [http://serialsjournals.com/articles.php?volumesno\\_id=447&journals\\_id=161&volumes\\_id=150](http://serialsjournals.com/articles.php?volumesno_id=447&journals_id=161&volumes_id=150)( Journal)
3. <http://link.springer.com/article/10.1007/s11042-016-3736-0>

### Year 2015-2016 Journal Publications

- K. Sathish Shet, A. R. Aswath, M. C. Hanumantharaju, Xiao-Zhi Gao “Design and development of new reconfigurable architectures for LSB/multi-bit image steganography system” Multimedia Tools and Applications, springer publications. M.C. et al. Multimed Tools Appl (2016). doi:10.1007/s11042-016-3736-0

#### a. Year: 01-01-2015 to 31-12-2015

#### Conferences: International

Sl. No	Name of Co-Author	Title of Paper	Name of the Conference	Publisher	Publication citation ISSN, ISBN, Vol. No., Issue, pp
1	Prasanna Kumar Patil	Implementation of an image steganography technique using x(x-or)-box mapping	International Journal of Advances in Electronics and Computer Science,	Institute of Technology & Research(IRAJ)	ISSN: 2393-2835 Volume-2, Issue-8, Aug.-2015 PP 32-36
2	RaviKiran R	Image Steganography Using Histogram Requantization	International Journal of VLSI System Design and Communication Systems	Institute of Technology & Research(IRAJ)	ISSN: 2393-2835 Volume-2, Issue-8, Pages- 0265-0270

**b. Year 01--01 -2014 to 31-12-2014**

Conferences: International

ii. K

Sl. No	Name of Co-Author	Title of Paper	Name of the Conference	Publisher	Publication citation ISSN, ISBN, Vol. No., Issue, pp
1 iii. iv. v. vi.	Nagavenp, Dr. A R Aswath	Steganography Using Integer Wavelet Transform Based on Color Space Approach.	Proceedings of the 3rd International Conference on Frontiers of Intelligent Computing: Theory and Applications (FICTA) 2014, © Springer International Publishing Switzerland 2015,	Presented and Published in the Proceedings of FICTA Springer November 2014	(FICTA) 2014 Vol. 1, Advances in Intelligent Systems and Computing 327  DOI: 10.1007/978-3-319-11933-5_95,  pp .839-848

**c. Year: 01-01-2013 to 31-12-2013**

ii. conferences: International

Sl. No	Name of Co-Author	Title of Paper	Name of the Conference	Publisher	Publication citation ISSN, ISBN, Vol. No., Issue, pp
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1	Alka Singh, Dr A R aswath	Hardware Architecture for ImageSteganography based on LSB Tecnique.	International conference on Electrical Engineering and Computer science (EECS	Interscience Open Access Journal	ISSN -978-93-83060-04-7,  Volume No:1 Issue 2  Month & Year: June 2013  pp. 16-19.
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**d. Year 01-01 -2012 to 31-12-2012**

**i. Journal International:**

<b>Sl. No.</b>	<b>Name of Co-Author</b>	<b>Title of Paper</b>	<b>Name of the Journal</b>	<b>Publisher</b>	<b>Publication citation ISSN, ISBN, Vol. No., Issue, pp</b>
1	Sanjay K Nagendra, Dr A R Aswath	An ARM Based Implementation of Adaptive DCT Based Steganography	International Journal of Operations system & human resource management , International Science press	International Science press	ISSN : 2277-6095 Volume No. :2 Issue :1-2 Month & Year : December 2012 Page Nos. : 59-68

**vii. Conferences: International**

<b>Sl. No</b>	<b>Name of Co-Author</b>	<b>Title of Paper</b>	<b>Name of the Conference</b>	<b>Publisher</b>	<b>Publication citation ISSN, ISBN, Vol. No., Issue, pp</b>
1	Sanjay Nagendra, Dr A R aswath	An ARM based implementation of adaptive DCT based steganography	ICCIIT- 2012 Info Institute of Engg., Coimbatore	Umayam Publications	ISBN 81-903041-9-4 2nd -3rd March 2012

vii.	Sanjay Nagendra, Dr A Raswathi o p s	A Theoretical survey on SOC based Implementaion of Digital Image Steganography	Second International Confrence on Control, Communication and Computer Technology, Bengalo re	Interscience Research Network(India), Bhubaneswar,	Conference Proceedings IRNet
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