



JSS MAHAVIDYAPEETHA

JSS ACADEMY OF TECHNICAL EDUCATION

JSSATE Campus, Uttarahalli – Kengeri Main Road, Bangalore – 560 060

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1. **NAME** : Kavitha H S
2. **Designation** : Assistant Professor
3. **Name of the Dept.** : Electronics and Communication
4. **Date of joining the Institution:** 21-08-2006
5. **Date of Birth** : 18-04-1980
6. **Father's Name** : Shankara H
7. **Sex** : Female
8. **Nationality** : INDIAN
9. **Contact No.** : 9686599335
10. **E-mail ID** : kavi.hs@gmail.com
 - a. **Correspondance** : #G3, V G Enclave, Doctors Layout, Simha Colony,
Chickalasandra Main Road
BENGALURU 560061
 - b. **Permanent Address** : D/o Shankara ,
S.Hosakote, Nanjanagud Taluk,
Mysore District.
11. **Employment Type** : Permanent
12. **Educational Qualifications** : **B.E., M.E.,**
13. (In reverse Chronological Order)



Sl. No	Degree	Branch	Specialization	Year of passing	University
1	M.Tech.,	Electronics & Communication Engineering	Industrial Electronics	January 2007	VTU, Belgaum
3	B.E.,	Electrical and Electronics	Electrical	January 2002	

14. **Service / Experience Details** : 30 years
(In reverse Chronological Order)

a. **Teaching Experience:**

Sl. No.	From (dd-mm-yy)	To (dd-mm-yy)	No. of years	Degree or Diploma Institution	Designation	Name of the Institute
1	21-08-2006	Till Date	10	Degree	Assistant Professor	JSSATE, Bangalore
2	June-2004	June 2006	2	Degree	Visiting Faculty	SJCE, Mysore

Total Experience	12 Years	
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b. Industrial Experience:

Sl. NO	From	To	No. of years	Designation	Name of the Company
1					

15. Membership in various University Boards / Committees / Other (Specify):

Sl. No.	Body	From	To	Name of the University / Institution / Organisation
1				
2				
3				
4				
5				

15. Awards : -.

16. Details of Books published : -

17. Number of Ph.D. Guidance : -

18. Field of Expertise / Interest : Power Electronics

19. Patents :

20. Projects

Sl. No.	Name of the project	Principal Investigator / Co- Investigato	Amount sanctioned From	Amount	Date of start of the project	Date of completion of the project	Outcome of the project
1							
2							
3							
4							

:

21. Details of Publications:

Google scholar link:

As on 30-08-2016

Citation Indices	All	Since 2011
Citations		
h-Index		
i10-Index		

Number of papers published in International Journals :

Number of papers published in National Journals : 2

Number of papers published in International Conferences : 2

Number of papers published in National Conferences : 2

a. Journals: International

Sl. No.	Name of Co-Author	Title of Paper	Name of the Journal	Publisher	Publication citation ISSN, ISBN, Vol. No., Issue, pp
i. 1					
ii. 2					

b. Journals: National

Sl. No	Name of Co-Author	Title of Paper	Name of the Journal	Publisher	Publication citation ISSN, ISBN, Vol. No., Issue, Impact Factor, pp
1	Shivakumar M	Analysis of Random Arbiter Router Design for NOC			
2	Kavitha H S	High Efficiency Single Input Multiple Output Dc-Dc Converter Paper			

c. Conferences: International

Sl. No	Name of Co-Author	Title of Paper	Name of the Conference	Publisher	Publication citation ISSN, ISBN, Vol. No., Issue, pp
1	CHANDANA GOPAL.K, ASHWINI.S	POWER INTEGRATED CIRCUITS	ICRTSIV		
2	Shivakumar M	FPGA Implementation and Performance Analysis of conventional and Modified Router Design for NOC			

d. National conferences / Symposium / workshops

Sl. No	Name of Co-Author	Title of Paper	Name of the Conference	Publisher	Publication citation ISSN, ISBN, Vol. No., Issue, pp
1	Ananth M	Implementation of QRS Detector Based on Adaptive Threshold Technique	NCVSIP		
2	Ananth M	VHDL Implementation Of QRS Detector for Heart Arrhythmia Detection Based On Adaptive Threshold Technique			