



JSS MAHAVIDYAPEETHA

JSS ACADEMY OF TECHNICAL EDUCATION

JSSATE Campus, Uttarahalli – Kengeri Main Road, Bangalore – 560 060

Phone: 080-28611902, 28612797 Fax: 080-28612706 www.jssateb.ac.in

1. **NAME** : GOURI S KATAGERI
2. **Designation** : ASST PROF
3. **Name of the Dept.** : ELECTRONICS AND COMMUNICATION
4. **Date of joining the Institution:** 23/07/2012
5. **Date of Birth** : 12-08-1986
6. **Father's Name** : SHIVARUDRAPPA R KATAGERI
7. **Sex** : Female
8. **Nationality** : INDIAN
9. **Contact No.** : 9986831531
10. **E-mail ID** : gouriskatageri@gmail.com
 - a. **Correspondence** : GOURI S KATAGERI,
ASST. PROF, ECE DEPARTMENT
UTTARAHALLI-KENGERI MAIN ROAD,
BENGALURU 560060
 - b. **Permanent Address** : GOURI S KATAGERI,
#607, ARCHITA AROHI APARTMENTS, RR NAGAR
BANGLORE- 560098
11. **Employment Type** : Permanent
12. **Educational Qualifications** : B.E., M.Tech
(In reverse Chronological Order)



Sl. No	Degree	Branch	Specialization	Year of passing	University
1	M.Tech	Electronics & Communication Engineering	VLSI Design and Embedded Systems	2011	VTU
2	B.E.,	Electrical	Electrical	2008	

13. **Service / Experience Details** : 4.6 years
(In reverse Chronological Order)

a. **Teaching Experience:**

Sl. No.	From (dd-mm-yy)	To (dd-mm-yy)	No. of years	Degree or Diploma Institution	Designation	Name of the Institute
1	23-07-2012	Till date	4 years	Degree	ASST PROF	JSS Academy of Technical Education, Bengaluru 560 060

2	07-02-2012	21-06-2012	0.5 Years	Degree	LECTURER	SJCE MYSORE
Total Experience			4.5 Years			

b. Industrial Experience:

Sl. NO	From	To	No. of years	Designation	Name of the Company
1					

14. Membership in various University Boards / Committees / Other (Specify):

Sl. No.	Body	From	To	Name of the University / Institution / Organisation
1	Board of Examiners			
2	Board of Studies			
3	Academic Senate			
4	Executive Council			
5	LIC MPCC			

15. Awards

: 16. Details of Books published :

17. Number of Ph.D. Guidance :

18. Field of Expertise / Interest : VLSI DESIGN, ASIC DESIGN

Digital Circuits & Logic Design

19. Patents :

20. Projects

Sl. No.	Name of the project	Principal Investigator / Co-Investigator	Amount sanctioned From	Amount	Date of start of the project	Date of completion of the project	Outcome of the project
1							
2							
3							
4							

21. Details of Publications:

Google scholar link: : <https://scholar.google.co.in/citations?user=XQdKV4AAAAAJ&hl=en>

Details of Publications: As on 15-07-2016

Citation Indices	All	Since 2010
Citations	NIL	NIL
h-Index	NIL	NIL
i10-Index	NIL	NIL

Number of papers published in International Journals : 01

Number of papers published in National Journals : -

Number of papers published in International Conferences : -

Number of papers published in National Conferences : 01

a. Journals: International

Sl. No.	Name of Co-Author	Title of Paper	Name of the Journal	Publisher	Publication citation ISSN, ISBN, Vol. No., Issue, pp
1 i.	Aravind Guntaz	GPS and GSM Based Database Systems for User Access	International Journal of Software and Web Sciences (IJSWS)	International Association of Scientific Innovation and Research (IASIR) (An Association Unifying the Sciences, Engineering, and Applied Research)	ISSN (Print): 2279-0063 ISSN (Online): 2279-0071 Page NO- 24-28 Impact factor -5.88

b. Journals: National

SI. No	Name of Co-Author	Title of Paper	Name of the Journal	Publisher	Publication citation ISSN, ISBN, Vol. No., Issue, Impact Factor, pp
	NIL				

c. C

Conferences: International

SI. No	Name of Co-Author	Title of Paper	Name of the Conference	Publisher	Publication citation ISSN, ISBN, Vol. No., Issue, pp
	NIL				

d. N

National conferences / Symposium / workshops

SI. No	Name of Co-Author	Title of Paper	Name of the Conference	Publisher	Publication citation ISSN, ISBN, Vol. No., Issue, pp
1	Dr Uday Wali	EDA Tool for Netlist to Layout Conversion	National conference on VLSI, Signal and Image Processing	NCVSIP	