

JSS MAHAVIDYAPEETHA

JSS ACADEMY OF TECHNICAL EDUCATION

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2. Date of Birth : 16-06-19663. Father's Name : Gurunanjappa

4. Sex : Male5. Nationality : INDIAN

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8. Address

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KOLAR-563101

9. **Employment Type** : Permanent

10. Educational Qualifications : (In reverse Chronological Order)

SI. No.	Degree	Branch	Specialization	Year of passing	University
1	Ph.D.	Electronics & Communication Engineering	Signal Processing	pursuing	VTU Belagavi
2	M.E.,	Power Electronics	Power Electronics	Jan 2007	Bangalore university, Bengaluru
3	B.E.,	Electrical& Electronics	Electrical&Eelctronics	Jan 1993	Mysore university, Mysore

11. Service Details (In reverse Chronological Order):

a. Teaching Experience:

SI. No.	From (mm/yy)	To (mm/yy)	No. of years	Degree or Diploma Institution	Designation	Name of the Institute
1	Sept 2007	Till date	9 years	Degree	Assistant professor	JSS Academy of Technical Education, Bengaluru 560 060



2	March 2007	Sept 2007	0.6 Years	Degree	Senior Lecturer	Atria institute of Technology, Bengaluru
3	Aug 2003	March 2007	3.6 Years	Degree	Senior Lecturer	SCT institute of Technology
4	Nov 1997	Aug 2003	6 Years	Degree	Senior Lecturer	SBMSIT Nelamangala
5	Jan1993	Oct 1997	4.9 Years	Diploma	Lecturer	BPC Bangarapet
Total Experience 23 Years						

12. Details of Publications:

Number of papers published in International Journals : 04

Number of papers published in National Journals : Nil

Number of papers published in International Conferences : 02

Number of papers published in National Conferences : 01

Journals: International

SI. No	Name of Co-Author	Title of Paper	Name of the Journal	Publishe r	Publication citation ISSN, ISBN, Vol. No., Issue, pp
1	Manjula	optimized modulo multipliers based on rns	AJER	AJER	ISSN : 2231- 2307 Volume No. :2 Issue :7 Month & Year : July2013 Page Nos. : 176-184
2	Manjunath	Efficient parallel FIR digital filter structure for symmetric convolution.	interscience open access journals	interscien ce	june 2013.
3	Madhukar Dr. D Jayadevappa	Over Coming of Errors in Tmr SystemUtilizing Scanchain Methods.	AJER	AJER	 Volum No: 05, Issue No: 03, Page Nos: 165- 170, Month & Year: March 2016.
4	Ajay kumar	Implimentation of sub-carrier Modulation Techniques using MATLAB/Simulink	IRJET	IRJET	1 ISSN: 2395-0056 Volume No. :3 Issue :5 Month & Year: May 2016

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ii. : International conference

Name of Co-**Title of Paper** Name of the **Publisher** SI. **Publication** Conference No **Author** citation ISSN, ISBN, Vol. No., Issue, pp Area power efficient for ICICE-3013 International conference: At July 2013 1 Manjula modulo multipliers based on Dr. AIT Bangalore rrns Manjunath Efficient parallel FIR digital EECS International conference: June 2013 2 ISSN: filter structure for symmetric published in interscience convolution. open access journals

National conferece

SI. No	Name of Co-Author	Title of Paper	Name of the Conference	Publisher	Publication citation ISSN, ISBN, Vol. No., Issue, pp
1	Ajith	Approximately linear phase digital filter bank with FRM technique		National conference : At JSSATE Bangalure	Aug 2014