



JSS MAHAVIDYAPEETHA

# JSS ACADEMY OF TECHNICAL EDUCATION

JSSATE Campus, Uttarahalli – Kengeri Main Road, Bangalore – 560 060

Phone: 080-28611902, 28612797 Fax: 080-28612706 [www.jssateb.ac.in](http://www.jssateb.ac.in)

1. **NAME** : Anuradha.M.G
2. **Designation** : Asst. Professor
3. **Name of the Dept.** : Electronics and Communication
4. **Date of joining the Institution:**21.01.2009
5. **Date of Birth** : 19-08-1980
6. **Father's Name** : H.S.Guruprasad
7. **Sex** : Female
8. **Nationality** : INDIAN
9. **Contact No.** : (M) +91 9845616698
10. **E-mail ID** : anuarun.19@gmail.com
11. **Address**



- a. **Correspondance** : Assistant Professor, Department of ECE  
JSS ACADEMY OF TECHNICAL EDUCATION,  
UTTARAHALLI – KENGERI MAIN ROAD  
BENGALURU 560060
- b. **Permanent Address** : #208, Satellite Splendor Apartment,  
Suncity Road, K.S.Town, Bangalore- 560060
12. **Employment Type** : Permanent
13. **Educational Qualifications** : (In reverse Chronological Order)

Sl. No.	Degree	Branch	Specialization	Year of passing	University
1	Ph.D.	Electronics & Communication Engineering	VLSI	Pursuing	VTU, Belgaum
2	M.Tech.	Electronics & Communication Engineering	VLSI Design and Embedded System	January 2009	
3	B.E	Electronics & Communication Engineering	Electronics & Communication Engineering	September 2002	

14. **Service Details** (In reverse Chronological Order):

**a. Teaching Experience:**

Sl. No.	From (mm/yy)	To (mm/yy)	No. of years	Degree or Diploma Institution	Designation	Name of the Institute
1	21-01-2009	Till date	7.9 years	Degree	Asst. Professor	JSS Academy of Technical Education, Bengaluru 560 060
2	7-02-2007	19-01-2009	2 Years	Degree	Lecturer	Don Bosco Institute of Technology, Bengaluru
3	29-8-2005	4-02-2007	1.7 Years	Degree	Lecturer	Vidya Vardhaka College of Engineering, Mysore
4	12-7-2004	27-8-2005	1.1 Years	Diploma	Lecturer	Vidya Vikas Educational Trust, Mysore
5.	1-8-2002	11-7-2004	2 Years	Institute	Lecturer	Chanakya Institute of Professional Studies, Bengaluru
<b>Total Experience</b>			<b>14.7 Years</b>			

**b. Industrial Experience:**

Sl. NO.	From	To	No. of yrs.	Designation	Name of the Company
1	-----	-----	-----	-----	-----

**15. Membership in various University Boards / Committees / Other (Specify):**

Sl. No.	Body	From	To	Name of the University / Institution / Organisation
1	Board of Examiners	Nil	Nil	Nil
2	Board of Studies	Nil	Nil	Nil
3	Academic Senate	Nil	Nil	Nil
4	Executive Council	Nil	Nil	Nil
5	LIC                      MPCC	Nil	Nil	Nil

16. Awards

17. Details of Books published : HDL Manual, Subhas publications, Feb-2014

18. Number of Ph.D. Guidance : Nil

19. Field of Expertise / Interest : Digital and Analog VLSI , Analog Circuits,

20. Patents : Nil

21. Projects

<b>Sl. No.</b>	<b>Name of the project</b>	<b>Principal Investigator / Co-Investigator</b>	<b>Amount sanctioned From</b>	<b>Amount</b>	<b>Date of start of the project</b>	<b>Date of completion of the project</b>	<b>Outcome of the project</b>
1	Nil	Nil	Nil	Nil	Nil	Nil	Nil
2	Nil	Nil	Nil	Nil	Nil	Nil	Nil
3	Nil	Nil	Nil	Nil	Nil	Nil	Nil
4	Nil	Nil	Nil	Nil	Nil	Nil	Nil

22. Details of Publications: Google scholar link: [http://scholar.google.co.in/citations?view\\_op=list\\_works&hl=en&user=0Gh6DnoAAAAJ](http://scholar.google.co.in/citations?view_op=list_works&hl=en&user=0Gh6DnoAAAAJ)

As on 30-11-2015

Citation Indices	All	Since 2010
Citations	0	0
h-Index	0	0
i10-Index	0	0

a. Year: 01-01-2016 to 31-12-2016

i. Conferences: National

Sl. No	Name of Co-Author	Title of Paper	Name of the Conference	Publisher	Publication citation ISSN, ISBN, Vol. No., Issue, pp
1. a. <u>J</u>	Chaitra.M, Puneeth S B  <u>o</u> <u>u</u>	Implementation of Vedic Multiplier using Different Parallel Prefix adders	Natiional Conference on " Recent trends in Electronics and Communication Engineering(NCREC-2016) held on 5 <sup>th</sup> May 2016	SKIT, Bengaluru	

**b. Year: 01-01-2015 to 31-12-2015**

i. Journals: International

Sl. No	Name of Co-Author	Title of Paper	Name of the Journal	Publisher	Publication citation ISSN, ISBN, Vol. No., Issue, pp
C. 1	Lakshmikanth.M.N	Design of low power arithmetic and logic unit using reversible logic gates	International Journal of VLSI System Design and communication system	SEMAR groups, Hyderabad, India	1JVDCSV03IS03P4883-2

ii. National conferences / workshops

Sl. No	Name of Co-Author	Title of Paper	Name of the Workshop	Publisher	Publication citation ISSN, ISBN, Vol. No., Issue, pp
1	iii. Y		Robovision workshop		20 <sup>th</sup> and 21 <sup>st</sup> July 2015, conducted by Technophilia Systems in Association with robotics and computer application Institute of USA held at JSS academy of Technical Education
2	e a r		AutoBotz AVR workshop		27 <sup>th</sup> and 28 <sup>th</sup> Feb 2015, conducted by Technophilia Systems in Association with robotics and computer application Institute of USA held at JSS academy of Technical Education
3			Signal, Image processing & SDR using Labview		22 <sup>nd</sup> -24 <sup>th</sup> June 2015, organized by JSSATE, Bangalore

c. 01-01-2014 to 31-12-2014

i. Conferences: National

Sl. No	Name of Co-Author	Title of Paper	Name of the Conference	Publisher	Publication citation ISSN, ISBN, Vol. No., Issue, pp
1. ii.N	Ajay.J a t i o	Roll Attitude Hold and Fault Analysis Using Kalman Filter for Flight Control System	Natiional Conference on “ Developments in the domain of Electrical Engineering(NCDDEE -14) held on 10 <sup>th</sup> and 11 <sup>th</sup> July 2014	SSIT, Tumkur in association with The Institution of Engineers	ISBN:978-81-927765-0-3 pp-7-9
2.	Ajay.J h a t j	Aircraft Heading & Roll Autopilot Design with Fault Analysis Using Kalman Filter	National Conference on “ VLSI,Signal and Image processing” held on 22 <sup>nd</sup> and 23 <sup>rd</sup> August 2014.	JSS Academy of Technical Education	

onal conferences / workshops

Sl. No	Name of Co-Author	Title of Paper	Name of the Conference	Publisher	Publication citation ISSN, ISBN, Vol. No., Issue, pp
1.			Workshop on “Research Methodology and Latex”		Organized by VTU e-learning centre, Mysore from 9 <sup>th</sup> to 11 <sup>th</sup> September 2014 at Shree Devi Institute of Technology, Mangalore.
2.			National Conference on “ VLSI,Signal and Image processing” held on 22 <sup>nd</sup> and 23 <sup>rd</sup> August 2014.		Organized by Department of ECE, JSSATE from 22 <sup>nd</sup> to 23 <sup>rd</sup> Aug 2014.
3.			Faculty development program on “ Advanced VLSI design using Cadence Tool suite”		Organized by Department of ECE, JSSATE from 16 <sup>th</sup> to 18 <sup>th</sup> July 2014.

**d. 01-01-2013 to 31-12-2013**

**i. Journals: International**

<b>Sl. No.</b>	<b>Name of Co-Author</b>	<b>Title of Paper</b>	<b>Name of the Journal</b>	<b>Publisher</b>	<b>Publication citation ISSN, ISBN, Vol. No., Issue, pp</b>
1.	Abhilash.B.T and Veerabadrappa.S.T	Design and Implementation of concurrent error detection and data recovery architecture for motion estimation testing application	International journal of embedded systems and soft computing	IJESC Journal publication	ISSN: 2250-1371 Vol:2 Issue:8 pp:1-7

**ii. Conferences: International**

<b>Sl. No</b>	<b>Name of Co-Author</b>	<b>Title of Paper</b>	<b>Name of the Conference</b>	<b>Publisher</b>	<b>Publication citation ISSN, ISBN, Vol. No., Issue, pp</b>
1.	Arun Kumar.R, Dr.Siva Yellampalli	VLSI Implementation of Multidimensional K means Clustering Algorithm for Multimedia Content Analysis	International Conference on "Pattern recognition Applications and techniques" held at Meenakshi College for Women, Chennai on 1 <sup>st</sup> and 2 <sup>nd</sup> March 2013	Tata Mac-Graw hill publications	ISBN: 978-1-25-905849-3

**iii. Conferences: National**

<b>Sl. No</b>	<b>Name of Co-Author</b>	<b>Title of Paper</b>	<b>Name of the Conference</b>	<b>Publisher</b>	<b>Publication citation ISSN, ISBN, Vol. No., Issue, pp</b>
1.	Poornima.B.K	VLSI architecture for Image Segmentation using Fuzzy C means Algorithm	NCRTEC-13 held on 30 <sup>th</sup> August 2013	KSIT, Bangalore	

iv. National conferences / workshops

<b>Sl. No</b>	<b>Name of Co-Author</b>	<b>Title of Paper</b>	<b>Name of the Conference</b>	<b>Publisher</b>	<b>Publication citation ISSN, ISBN, Vol. No., Issue, pp</b>
1.			Workshop on "VLSI design lab using open source"		Organized M.S.Engineering College from 24 <sup>th</sup> to 25 <sup>th</sup> July 2013, Bengaluru.

e. 01-01-2012 to 31-12-2012

i. Conferences: International

<b>Sl. No</b>	<b>Name of Co-Author</b>	<b>Title of Paper</b>	<b>Name of the Conference</b>	<b>Publisher</b>	<b>Publication citation ISSN, ISBN, Vol. No., Issue, pp</b>
1.	Vani.A	Synthesis static timing analysis and formal verification methodology for IPs in CSOC	ICEEE- 2012, Mysore, on 30 <sup>th</sup> June 2012		ISBN: 978-1-25-905849-3



ii. National conferences / workshops

<b>Sl. No</b>	<b>Name of Co-Author</b>	<b>Title of Paper</b>	<b>Name of the Conference</b>	<b>Publisher</b>	<b>Publication citation ISSN, ISBN, Vol. No., Issue, pp</b>
1.			Faculty Development program on "Project management concepts"		Organized by Cognizant, Bangalore on 27 <sup>th</sup> and 28 <sup>th</sup> April 2012.