



JSS MAHAVIDYAPEETHA

JSS ACADEMY OF TECHNICAL EDUCATION

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1. **NAME** : Ms. HARSHITHA S
2. **Designation** : **ASSISTANT PROFESSOR**
3. **Name of the Dept.** : **ELECTRONICS and INSTRUMENTATION ENGINEERING**
4. **Date of joining the Institution:** **29-07-2015**
5. **Date of Birth** : 28-05-1990
6. **Father's Name** : Mr. N S SHIVAPRASAD
7. **Sex** : Female
8. **Nationality** : INDIAN
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 - a. **Correspondence** : No. 101, 3rd Main Road, JNANAGANGANAGAR
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 - b. **Permanent Address** : No. 101, 3rd Main Road, JNANAGANGANAGAR
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11. **Employment Type** : Permanent
12. **Educational Qualifications** : **B.E., M.Tech.**



Sl. No	Degree	Branch	Specialization	Year of passing	University
1	M.Tech.	Electronics & Communication Engineering	Signal Processing and VLSI	2014	Jain University, Bengaluru
2	B.E.	Electronics & Communication Engineering	Electronics & Communication Engineering	2011	Visvesvaraya Technological University, Belagavi

13. Service / Experience Details : 02 years

a. Teaching Experience:

Sl. No.	From (dd-mm-yy)	To (dd-mm-yy)	No. of years	Degree or Diploma Institution	Designation	Name of the Institute
1	29-07-2015	Till date	1.25 Years	Degree	Assistant Professor	JSS Academy of Technical Education, Bengaluru 560 060

b. Industrial Experience:

Sl. NO	From	To	No. of years	Designation	Name of the Company
1	05-12-2011	07-09-2011	0.75 years	Assistant Systems Engineer	Tata Consultancy Services Ltd.
Total Experience			02 Years		

14. **Membership in various University Boards / Committees / Other (Specify): Nil**

15. Awards : Nil

16. Details of Books published: Nil

17. Number of Ph.D. Guidance : Nil

18. Field of Expertise / Interest : Digital Signal and Image Processing,
Digital Logic Design, VLSI Design

19. Patents : Nil

20. Projects : Nil

21. Details of Publications:

Number of papers published in International Journals : 02

Number of papers published in National Journals : 00

Number of papers published in International Conferences : 00

Number of papers published in National Conferences : 01

a. Journals: International

Sl. No.	Name of Co-Author	Title of Paper	Name of the Journal	Publisher	Publication citation ISSN, ISBN, Vol. No., Issue, pp
i. 1 ii.	Mr. Gopalakrishna K	'A 3-Stage Pipeline VLSI Architecture for Fast Computation of the 2-D Discrete Wavelet Transform'	'International Journal of Science and Research'		ISSN (Online) : 2319-7064 Volume No. :3 Issue :7 Month & Year : July 2014 Page Nos. : Impact factor :
2	iii. Mr. Gopalakrishna K iv.	'Comparative Performance Analysis of a High Speed 2-D Discrete Wavelet Transform using Three different Architectures'	'International Journal of Emerging Trends & Technology in Computer Science'		ISSN : 2278-6856 Volume No. :3 Issue :4 Month & Year : August 2014 Page Nos. :223-234 (12) Impact Factor :

b. National conferences / Symposium / workshops

Sl. No	Name of Co-Author	Title of Paper	Name of the Conference	Publisher	Publication citation ISSN, ISBN, Vol. No., Issue, pp
1	Pallavi B Vishnu Priya G L	Noise Cancellation in EMG signal using Adaptive filter	"National Conference on Embedded Systems and Signal Processing"		Organized by the Dept. of Electronics & Communication Engineering, School of Engineering and Technology, Jain University, Bangalore