



JSS MAHAVIDYAPEETHA

# JSS ACADEMY OF TECHNICAL EDUCATION

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1. **NAME** : Mrs. Bhanumathi K S
2. **Designation** : Assistant Professor
3. **Name of the Dept.** : Electronics & Instrumentation Engineering
4. **Date of joining the Institution:** 21-07-2001
5. **Date of Birth** : 24-11-1978
6. **Father's Name** : Shivanna
7. **Sex** : Female
8. **Nationality** : INDIAN
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  - b. **Permanent Address** : #7, 5<sup>th</sup> 'A' Cross, Shivapura, Bagalur Main Road  
Yelahanka, Bengaluru- 560064
11. **Employment Type** : Permanent
12. **Educational Qualifications** : **B.E., M.E., Ph.D.,**  
(In reverse Chronological Order)



Sl. No	Degree	Branch	Specialization	Year of passing	University
1	(Ph.D)	Electronics & Instrumentation Engineering	Biomedical Signal Processing	Pursuing	Visvesvaraya Technological University
2	M.Tech	Electronics & Communication Engineering	VLSI design & Embedded Systems	Jan 2012	Visvesvaraya Technological University
3	B.E.,	Instrumentation Technology	Instrumentation	January 2001	Mysore University

**13. Service / Experience Details : 11 years**

**a. Teaching Experience:**

Sl. No.	From (dd-mm-yy)	To (dd-mm-yy)	No. of years	Degree or Diploma Institution	Designation	Name of the Institute
1	10-06-2005	Till date	11 years	Degree	Assistant Professor	JSS Academy of Technical Education, Bengaluru 560 060
<b>Total Experience</b>			<b>11 Years</b>			

14. **Membership in various University Boards / Committees / Other (Specify): NIL**

15. Awards : NIL

16. Details of Books published : NIL

17. Number of Ph.D. Guidance : NIL

18. Field of Expertise / Interest : Signal Processing, VLSI, Analog Integrated Circuits,  
Network Analysis, Low power VLSI

19. Patents : NIL

20. Projects : NIL

21. Details of Publications : NIL