

**Details of Ph.D guided by Dr. Jayadevappa D.**

<b>Sl. No.</b>	<b>Name of the Research scholar</b>	<b>Year of Registration</b>	<b>Registered for</b>	<b>Topic</b>	<b>Status</b>
1	G Parameshwarappa	Jan 2012	Ph.D	Filter bank design	Implementation under progress
2	Manjula C	Oct 2013	Ph.D	FPGA based scalable universal digital automated test equipment	Implementation under progress
3	Duney D Sam	Dec 2013	Ph.D	Wireless data logger efficient energy management system smart grids using FPGA	Implementation under progress
4	Bhanumathi K S	Dec 2014	Ph.D	Real time EEG based basic drives detection through human emotion recognition	Yet to take course work
5	Sharan Kumar	Dec 2014	Ph.D	Development of Automatic Segmentation of Brain MRI Images Using Deformable Models and Performance Evaluation	Yet to take course work

**Details of Ph.D guided by Dr. D Mahesh Kumar**

<b>Sl. No.</b>	<b>Name of the Research scholar</b>	<b>Year of Registration</b>	<b>Registered for</b>	<b>Topic</b>	<b>Status</b>
1	V Vijayalakshmi	Feb 2016	Ph.D	Developing an algorithm for quantifying and assessing salt affected soils using remote sensing data	Yet to take course work
2	Chithra	Feb 2016	Ph.D	Design and development of vein patency detection system for medical applications	Yet to take course work